MASTER SEMICONDUCTOR VLSI COURSE With Placement

Build In-Demand Professional Expertise with Real-Time Hands-on Semiconductor VLSI Training

BEST RATED ON TOP PLATFORMS

Google Reviews ★★★★★ · Trustpilot ★★★★★ · Sulekha ★★★★★

COURSE BENEFITS

- 100% Practical & Real-Time Hands-on Labs
- End-to-End Capstone Implementation Projects
- Industry Recognized Certification Preparation
- 100% Dedicated Placement Support in MNC's & Mock Interviews

15+ YEARS OF EXCELLENCE
500+ HIRING PARTNERS
25K STUDENTS TRAINED
15K STUDENTS PLACED

Chat on WhatsApp
FREE Career Counselling
+91 85500 00909

YOUR FUTURE
STARTS HERE !

ARE YOU FACING THESE CHALLENGES ?

- Still searching for your first job after graduation?
- Worried about the Gap? And don't know how to start your career?
- Stuck in a low-salary job? and seeking for better growth opportunities?
- Fear of switching to an IT career from a non-IT background
- Not getting interview calls despite applying to 100+ jobs?

TOO MUCH THEORY
NO PROJECTS
SKILL GAP
NO GUIDANCE

How Inventateq Helps You

Waiting Ends. Careers Begin.
Build job-ready skills. Start applying with confidence.

Turns Gaps into Comebacks
Close skill gaps. Rebuild your career

Break Free. Move Forward
Learn in-demand skills. Take your next career step.

Doubt Out. Confidence In.
Practice real skills. Walk into interviews prepared.

Turn Rejections into Chances
Turn every setback into skills employers value.

Be Smart Learn AI
Learn AI tools. Work smarter. Stay updated

ABOUT INVENTATEQ

Inventateq is a leading IT training and placement company committed to transforming learners into Industry ready professionals

Our Mission
To deliver high-quality education, practical slots and career opportunities that empower individuals to achieve their dreams

Our Vision
To be a globally recognised training company known for innovation, quality and impactful learner outcomes.

Why Thousands Choose Inventateq?

15+ Years of Excellence
500+ Hiring Partners
25,000+ Students Trained
15,000+ Students Placed

We Collaborate With

Deloitte. · Capgemini · amazon · HCL · Tech Mahindra

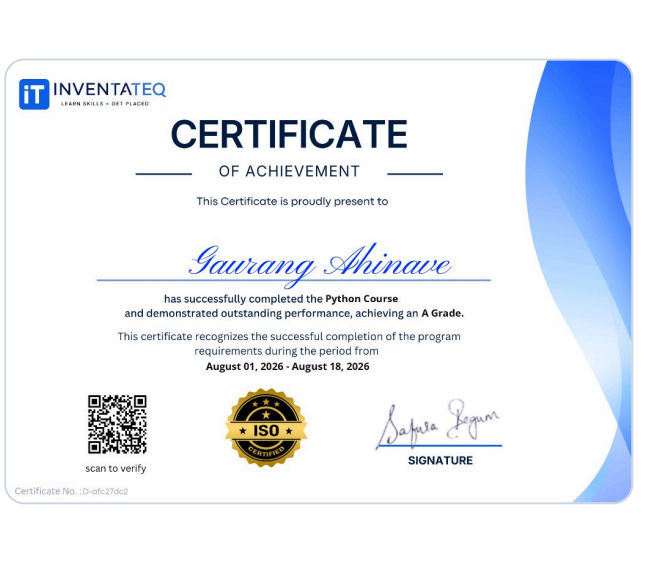
WHY LEARN SEMICONDUCTOR VLSI ?

Enterprises worldwide are actively hiring certified Semiconductor VLSI professionals to drive critical business operations, digital transformation, and modernization.

CAREER SCOPE OF SEMICONDUCTOR VLSI

- **Exponential Industry Demand**
  Rapid enterprise adoption across global IT firms, MNCs, and technology centers
- **Premium Salary Compensation**
  Command competitive compensation packages with rapid growth milestones
- **Cross-Industry Opportunities**
  Versatile skill set applicable across technology, consulting, finance, and core sectors
- **Future-Proof Career Growth**
  Long-term job security supported by ongoing digital modernization and tooling investment
- **Global Certification Pathways**
  Direct alignment with recognized industry certifications to validate your competence
- **Worldwide Job Prospects**
  Open doors to roles in major Indian tech hubs as well as international remote positions

INDUSTRY-RECOGNIZED CERTIFICATION



INVENTATEQ
CERTIFICATE
OF ACHIEVEMENT
This Certificate is proudly present to
Gaurang Ahinave
has successfully completed the Python Course and demonstrated outstanding performance, achieving an A Grade.
This certificate recognizes the successful completion of the program requirements during the period from August 01, 2026 - August 18, 2026
scan to verify
ISO
SIGNATURE

QR Verified · Globally Valid · ISO 9001:2015

Core Engineering & Manufacturing Demand. Automotive & Infrastructure Roles.

Average Semiconductor VLSI Design Engineer Salary in India

₹5.0 LPA – ₹16 LPA

WHY INVENTATEQ ?

Because we have placed over 15,000+ students and we can place you as well

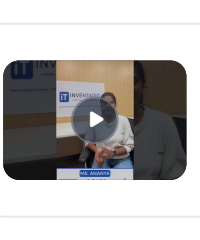
STUDENT PLACEMENTS

- 100% Placement Assurance Support
- 500+ Top Tech Hiring Partners
- Resume & LinkedIn Optimization
- Industry Mock Technical Interviews
- Unlimited Interview Calls Until Placed

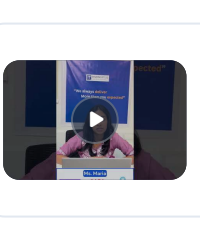
COURSE & PLACEMENT VIDEOS

Student Genuine Feedback
AWS Course in Bangalore, Best AWS Training &a...
Inventateq Bangalore Reviews
Watch Video

Practical Training Success
Inventateq Reviews & Student Genuine Feed...
Industry Tech Career
Watch Video



Placement Story
Ananya's Placement Success Story | AI Course ...
Campus to MNC Offer
Watch Video



Interview Skills & Growth
Inventateq HR Practical Training Student Succ...
Free Student Masterclass
Watch Video

> **At Inventateq, Your Success is Our Mission !**

GUARANTEED PLACEMENT SUPPORT

We don't just teach, we prepare you for the real world!

- Aptitude Training
- Technical Training
- Mock Interviews
- HR Interview Preparation
- Resume Preparation
- Cover Letter Preparation
- LinkedIn Profile Optimisation
- Job Portal Access
- Placement Drives & Hiring Events

OUR RECRUITMENT PARTNERS

TATA · Infosys · wipro · Capgemini · Tech Mahindra · LARSEN & TOUBRO · HCLTECH · accenture

OUR PLACEMENT STATS

Our placement record reflects consistent growth, with students securing opportunities across leading companies in diverse industries.

15,000+ Students Placed
500+ Companies Hired
95% Placement Ratio
6 LPA Average Package

Check Most Recent Placements

CAREER PROGRESSION

Job responsibilities, technical competency and portfolio evidence for each role.

ROLE PATHWAY	TYPICAL RESPONSIBILITIES	PORTFOLIO PROOF
Semiconductor VLSI Executive	Manage core Semiconductor tasks, configure tooling, and support delivery.	Practical Semiconductor setup files, exercise scripts, and documentation.
Semiconductor VLSI Specialist	Build practical Verilog HDL workflows, implement features, and resolve issues.	Functional Verilog HDL modules, workflow configs, and practical deliverables.
Senior Semiconductor VLSI Strategist	Lead Semiconductor VLSI technical delivery, design solutions, and set standards.	Complete Semiconductor VLSI capstone artifacts, technical blueprints, and reports.

SKILLS ROADMAP

Progress from core foundations to production-ready enterprise workflows.

MILESTONE	PRACTICAL OUTPUT	REVIEW EVIDENCE
Milestone 1 (Foundations)	Master fundamental Semiconductor concepts, environment setup, and tools.	Configured Semiconductor workspace and verified practice exercises.
Milestone 2 (Implementation)	Develop practical Verilog HDL workflows and functional domain modules.	Working Verilog HDL project modules and validated practice outputs.
Milestone 3 (Advanced Workflows)	Implement advanced Functional Verification features, integrations, and optimizations.	Validated Functional Verification configurations and technical review outputs.
Milestone 4 (Production Capstone)	Deliver an end-to-end Semiconductor VLSI capstone project and technical review.	Complete Semiconductor VLSI project portfolio artifact and defense.

DETAILED MODULE PLAN

8 in-depth modules • 3 Months Duration • Hands-on practical training

1 Module 1 – Semiconductor & VLSI Fundamentals

- Learning Objectives
- Understand the semiconductor industry, VLSI design flow, chip development lifecycle, and major
- job roles.
- Semiconductor Basics
- Semiconductor industry overview
- ASIC vs FPGA
- SoC fundamentals
- IP-based design

GUIDED LAB
Log in to the system, practice basic navigation, and complete your first Learning Objectives exercise.

INTERVIEW & ASSESSMENT
Explain what Learning Objectives is used for and why companies choose it in simple words.

2 Module 2 – Digital Design for VLSI

- Learning Objectives
- Build strong practical digital-design skills required for RTL development.
- Combinational Logic
- Logic gates
- Multiplexers
- Demultiplexers
- Encoders/decoders
- Comparators

GUIDED LAB
Build a simple decision script that checks user input and repeats actions automatically.

INTERVIEW & ASSESSMENT
Explain when to use loops versus if-else conditions in simple words.

3 Module 3 – Verilog HDL & RTL Design

- Learning Objectives
- Develop industry-ready RTL coding skills.
- Verilog Fundamentals
- Modules and ports
- Data types
- Operators
- Continuous assignments
- Procedural blocks

GUIDED LAB
Write a basic program to store user information, do simple math, and print the output.

INTERVIEW & ASSESSMENT
Explain the difference between text, numbers, and lists with simple everyday examples.

4 Module 4 – SystemVerilog for Design & Verification

- Learning Objectives
- Use SystemVerilog features commonly used in modern RTL and verification environments.
- SystemVerilog data types
- logic
- Arrays
- Structures
- Enumerations
- Interfaces

GUIDED LAB
Build a simple decision script that checks user input and repeats actions automatically.

INTERVIEW & ASSESSMENT
Explain when to use loops versus if-else conditions in simple words.

5 Module 5 – Functional Verification & Debugging

- Learning Objectives
- Understand how engineers verify RTL functionality and debug failures.
- Verification Fundamentals
- Verification planning
- Testbench architecture
- DUT
- Stimulus generation
- Monitor

GUIDED LAB
Complete a guided practical exercise on Learning Objectives and verify your results.

INTERVIEW & ASSESSMENT
Explain the core purpose of Learning Objectives and how you use it in real job roles.

6 Module 6 – Linux, Git & Automation for VLSI

- Learning Objectives
- Develop the command-line and automation skills expected in modern semiconductor teams.
- Linux
- File system
- Common commands
- Permissions
- Processes
- Environment variables

GUIDED LAB
Build a simple decision script that checks user input and repeats actions automatically.

INTERVIEW & ASSESSMENT
Explain when to use loops versus if-else conditions in simple words.

7 Module 7 – Logic Synthesis & Static Timing Analysis

- Learning Objectives
- Understand how RTL becomes a gate-level netlist and how timing quality is evaluated.
- Synthesis
- RTL elaboration
- Technology mapping
- Standard cells
- Gate-level netlist
- Constraints

GUIDED LAB
Complete a guided practical exercise on Learning Objectives and verify your results.

INTERVIEW & ASSESSMENT
Explain the core purpose of Learning Objectives and how you use it in real job roles.

8 Module 8 – Physical Design Fundamentals

- Learning Objectives
- Understand the back-end VLSI flow and how RTL decisions affect physical implementation.
- Physical Design Flow
- Netlist
- Floorplanning
- Power planning
- Placement
- Clock Tree Synthesis

GUIDED LAB
Complete a guided practical exercise on Learning Objectives and verify your results.

INTERVIEW & ASSESSMENT
Explain the core purpose of Learning Objectives and how you use it in real job roles.

REAL-WORLD PORTFOLIO PROJECTS

Practical industry scenarios, tools applied, and demonstrable project outcomes.

Project 1: Learning Objectives

What it is: Set up the core Semiconductor VLSI environment and configure essential business settings.

What you do: Initialize base configurations, create clean templates, and verify workflow controls.

Tools used: Semiconductor VLSI, Learning Objectives

Project outcome: Working project foundation configured and tested according to best practices.

Project 2: Physical Design Flow

What it is: Build practical Verilog HDL RTL modules to handle everyday business operations.

What you do: Create core functional components, validate input data, and prevent common errors.

Tools used: Semiconductor VLSI, Verilog HDL RTL, Learning Objectives

Project outcome: Functional business module ready to run and demo with zero manual errors.

Project 3: Floorplanning

What it is: Connect Functional Verification Debugging with Learning Objectives to automate data and team workflows.

What you do: Set up automated pipelines, link required services, and run end-to-end tests.

Tools used: Semiconductor VLSI, Functional Verification Debugging, Learning Objectives

Project outcome: Live automated workflow pipeline documented with clear usage guides.

Project 4: Semiconductor VLSI End-to-End Capstone

What it is: Build a complete, production-ready Semiconductor VLSI project bringing all skills together.

What you do: Design, code, test, and document a full project workflow from scratch to launch.

Tools used: Semiconductor VLSI, Logic Synthesis Static, Learning Objectives

Project outcome: Verified capstone project ready to showcase on your resume and interview calls.

ASSESSMENT FRAMEWORK

Module tests, practical evaluations, capstone and completion criteria.

COMPONENT	ASSESSMENT FOCUS	PASS EVIDENCE
Module Knowledge Quizzes	Core Semiconductor principles, industry terminology, and workflow concepts	Consistent passing score across syllabus unit evaluation tests
Lab Submissions & Practice Reviews	Hands-on Verilog HDL implementation, configuration, and practical exercises	Working lab deliverables satisfying practical evaluation rubrics
Hands-On Mini Projects	Independent problem-solving using intermediate Functional Verification techniques	Functional project artifacts addressing realistic domain scenarios
Final Production Capstone	End-to-end Semiconductor VLSI implementation, quality review, and delivery	Complete capstone submission with technical walkthrough review
Technical Mock Viva	Technical reasoning, troubleshooting approaches, and communication	Successfully defending project decisions in structured mock panels

RESUME, GITHUB & LINKEDIN

Evidence-based resume writing and professional project presentation.

- Live Semiconductor VLSI portfolio featuring SEO audit decks, keyword plans, and ad metrics.
- Detailed Semiconductor diagrams, component specifications, and end-to-end workflows.
- Visual project sheets, live demonstration walkthroughs, and verified functional outputs.
- Project documentation covering Verilog HDL setup, configuration steps, and test notes.
- Direct resume bullet points highlighting hands-on Semiconductor VLSI tools and technical outcomes.
- Structured practice for technical interview pitches, walkthroughs, and viva defense.

OUR STUDENT

Meet the learners who turned their goals into successful careers through dedication, skill, and continuous growth.

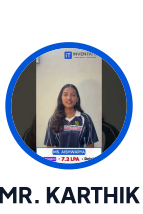
READ REVIEWS

MS. SNEHA
Placed at Wipro (9.5 LPA)

"The practical lab sessions gave me the confidence to transition into enterprise roles. Working on real-world datasets helped me clear technical interviews effortlessly!"
Course Completed : **Semiconductor VLSI Specialist** G ★★★★★

MR. HARSHIT
Placed at Accenture (11.2 LPA)

"Inventateq's focus on industry capstone projects and mock interviews was game-changing. The mentors guided me through every real-world implementation hurdle."
Course Completed : **Semiconductor VLSI Specialist** G ★★★★★



MR. KARTHIK
Placed at Capgemini (10.5 LPA)

"Moving from beginner theory to hands-on enterprise tools was seamless. The portfolio defense preparation helped me crack my MNC placement round!"
Course Completed : **Semiconductor VLSI Specialist** G ★★★★★

WATCH VIDEOS

Watch real stories from our students as they talk about their journey, growth, and placement success.

₹8.5 LPA

AWS Course in Bangalore, Best AWS Training & Certificate with Placement
Course Video 1 • Student Feedback
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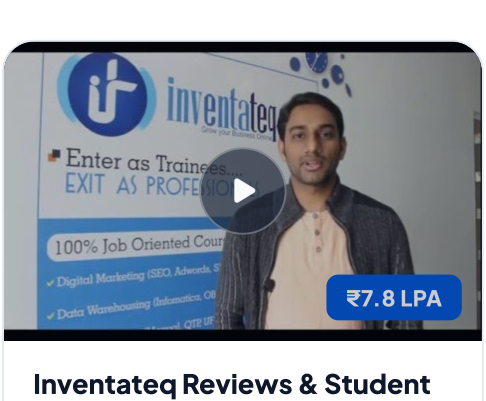
₹7.8 LPA

Inventateq Reviews & Student Genuine Feedback Bangalore
Course Video 2 • Student Feedback
Watch Story

₹9.2 LPA

Ananya's Placement Success Story | AI Course | 6.5 LPA at Accenture| Inventateq
Course Video 3 • Student Feedback
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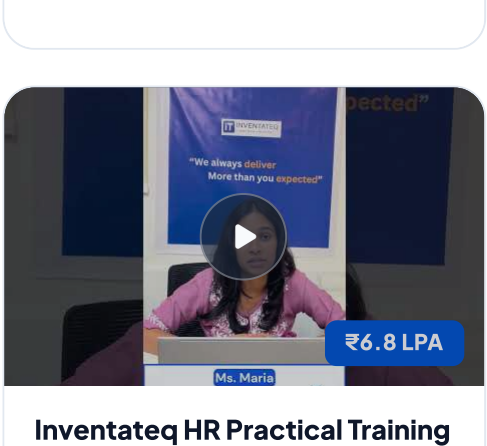
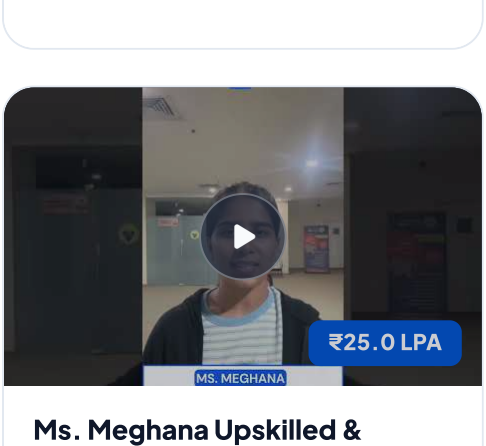
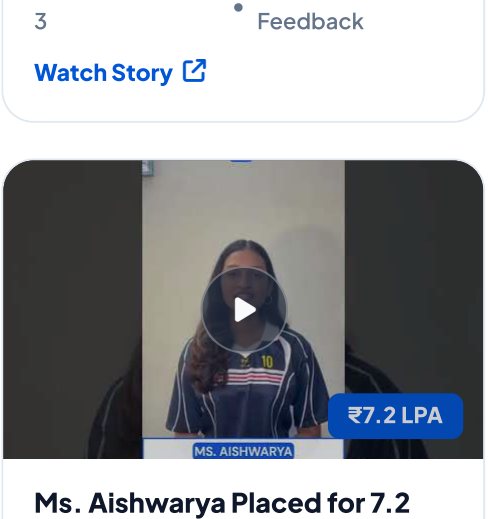

₹6.8 LPA

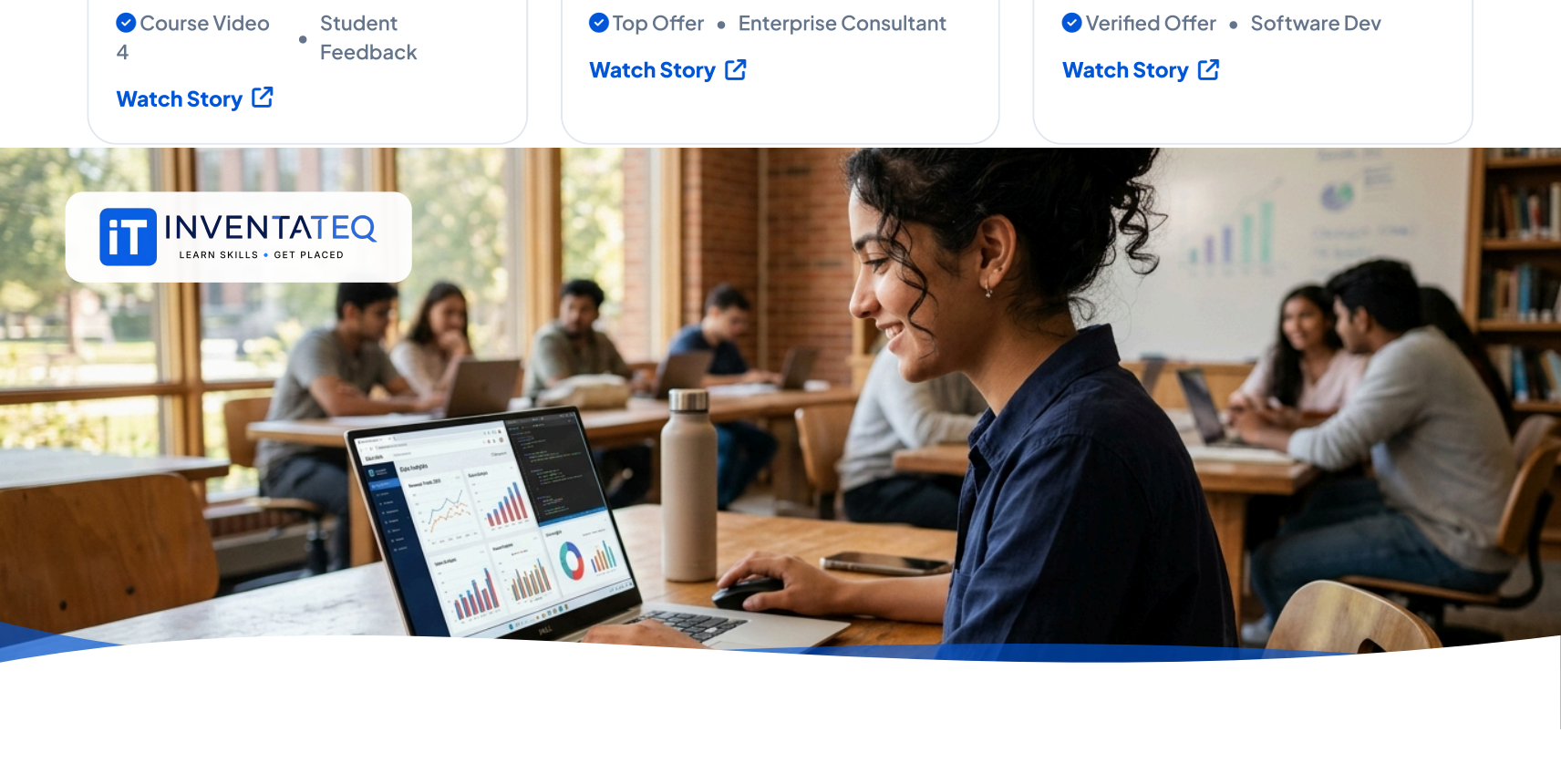
Inventateq HR Practical Training Student Success & Placement
Course Video 4 • Student Feedback
Watch Story


₹25.0 LPA

Ms. Meghana Upskilled & Placed at 25 LPA
Top Offer • Enterprise Consultant
Watch Story


₹7.2 LPA

Ms. Aishwarya Placed for 7.2 LPA Package
Verified Offer • Software Dev
Watch Story



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JOB HUNTING ENDS HERE
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